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*Supp.*  
*9/8*  
*Assessed*  
*10/30/03*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): S. NAKAMOTO, et al  
Serial No.: 10/083,254  
Filed: February 27, 2002  
For: SEMICONDUCTOR MANUFACTURING APPARATUS AND ITS  
DIAGNOSIS APPARATUS OPERATING SYSTEM  
Group: 2125  
Examiner: J. Gandhi

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Technology Center 2100

SUPPLEMENTAL AMENDMENT

Mail Stop Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

October 23, 2003

Sir:

Further to the Amendment filed September 12, 2003 in response to the Office Action dated April 14, 2003, the following supplemental amendments and remarks are respectfully submitted in connection with the above-identified application, as listed below and as set forth on the following pages:

Amendment of the Specification; and

Remarks are included following the amendments.